

Remarks

The final Office Action dated September 26, 2008 indicated that the drawings are objected to and that claims 1-6 stand rejected under 35 U.S.C. § 103(a) over applicant's admitted prior art ("APA", specification, page 1) in view of Tamai (U.S. Patent No. 6,580,180). It is Applicant's understanding that the objection to the drawings has been withdrawn per the Advisory Action dated December 8, 2008.

Applicant respectfully traverses the § 103(a) rejection of claims 1-6 because the cited portions of the '180 reference do not teach a logic circuit that switches a DC/DC converter on and off in response to the Office Action's alleged idle state as was discussed in detail in the Response dated November 24, 2008. Notwithstanding, in an effort to facilitate/expedite prosecution, Applicant has amended the claims to recite that the logic circuit (or the on-off logic circuit of claim 5) is configured to receive the DC input voltage to power the logic circuit. The cited portions of the '180 reference do not teach that the power supply terminal of MPU 110 (*i.e.*, the Office Action's alleged logic circuit) receives the voltage supplied by battery 12 (*i.e.*, the Office Action's alleged DC input voltage), instead MPU 110 is supplied with power from the second battery 13 and/or voltage converter 120 (*i.e.*, the Office Action's alleged DC/DC converter). *See, e.g.*, Figure 1 and Col. 5:12-18. Applicant notes that the Examiner acknowledges that APA does not disclose the claimed logic circuit. As such, Applicant submits that the cited combination of the '180 reference and APA does not correspond to the claimed invention. Accordingly, Applicant requests that the § 103(a) rejection of claims 1-6 be withdrawn.

Applicant has added new claims 7-12, which depend from either claim 1 or claim 5 and, as such, are allowable over the cited references for at least the reasons discussed above. Applicant has also added new claims 13-17, which contain aspects (similar to those of claims 1 and 5) directed to control circuitry that includes an input that is configured to receive the DC input voltage to power the control circuitry. None of the cited references teaches or suggests such aspects as discussed above in relation to the rejection of claims 1-6; as such, Applicant submits that claims 13-17 are allowable over the cited references. Applicant further submits that new claims 7-17 are fully supported

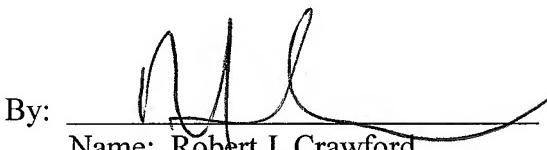
by Applicant's disclosure. *See, e.g.*, Applicant's Figure 1 and paragraphs 0019-0026 of Applicant's specification.

In view of the remarks above, Applicant believes that each of the rejections has been overcome and the application is in condition for allowance. Should there be any remaining issues that could be readily addressed over the telephone, the Examiner is asked to contact the agent overseeing the application file, Peter Zawilski, of NXP Corporation at (408) 474-9063 (or the undersigned).

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